

International Computer Symposium 2012 (ICS 2012)

12-14 December, Hualien, Taiwan

Workshop on Computer Architecture, Embedded Systems, SoC, and VLSI/EDA

Call for Papers

The 2012 Workshop on Computer Architecture, Embedded Systems, SoC, and VLSI/EDA, in conjunction with ICS 2012, will be held in Hualien, Taiwan, December 12-14, 2012. The main focus of the workshop will be on the modern design methodologies and techniques for computer architecture, embedded systems, system-on-chip, VLSI, and computer-aided design. This workshop will become a forum for researchers to summarize the most recent developments in the field, with a special emphasis given to the improvements and results. The proceedings of ICS 2012 will be published by Springer in series of Smart Innovation, Systems and Technologies (indexed by SCOPUS and Springerlink). The topics to be covered include, but are not limited to:

- Computer Architecture
- Parallel Systems
- Interconnection Networks
- Reconfigurable Architectures
- Embedded Systems
- Embedded Software
- VLSI Design
- System-on-Chip
- Network-on-Chip
- Signal Processing and Communication IC Design
- 3D Chip Design
- Analog/Mixed-Signal/RF Design
- Testing and Design for Testability
- Physical Design Automation
- Synthesis and Optimization
- Modeling and Verification

Before submission, authors should carefully read over the submission guidelines, located at <http://ics2012.ndhu.edu.tw>. All papers must be submitted in an electronic format which conforms to the workshop proceedings style described at the web site.

Prospective authors should submit an electronic

copy of their complete manuscript through the conference manuscript submission system at <http://ics2012.ndhu.edu.tw> according to the following timetable:

Manuscript Due	September 1, 2012 (Final)
Acceptance Notification	September 23, 2012
Camera-Ready Due	October 01, 2012

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Co-Chair:

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