

## **Workshop on Computer Architecture, Embedded Systems, SoC, and VLSI/EDA**

**Date:** Thursday, 13 Dec. 2012

**Room:** 3F 315.316.317

**Session:** Computer Architecture and Embedded Systems (4:30~6:00 pm)

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Chair: **Prof. Shyue-Ming Tang**, National Defense University, Taiwan

- 1051 On the Variants of Tagged Geometric History Length Branch Predictors  
*Yeong-Chang Maa and Mao-Hsu Yen*
- 1147 Hardware Acceleration Design for Embedded Operating System Scheduling  
*Jian-He Liao, Jer-Min Jou, Cheng-Hung Hsieh, and Ding-Yuan Lin*
- 1148 A Distributed Run-Time Dynamic Data Manager for Multi-core System Parallel Execution  
*Wen-Hsien Chang, Jer-Min Jou, Cheng-Hung Hsieh, and Ding-Yuan Lin*
- 1156 Design of a Dynamic Parallel Execution Architecture for Multi-Core Systems  
*Shiang Huang, Jer-Min Jou, Cheng-Hung Hsieh, and Ding-Yuan Lin*
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**Room:** 3F 308.309

**Session:** VLSI Design and Digital Signal Processing (4:30~6:00 pm)

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Chair: **Prof. Shun-Wen Cheng**, Far East University, Taiwan

- 1063 Energy-Aware Compiler Optimization for VLIW-DSP Cores  
*Yung-Cheng Ma, Tse-An Liu, and Wen-Shih Chao*
- 1067 A Multiplier-Free Noise Trapped Touch Algorithm for Low Cost 4x4 Matrix Panel Design  
*Yu-Hsaing Yu, Qi-Wen Wang, and Tsung-Ying Sun*
- 1160 A Novel Defragmentable Memory Allocating Schema for MMU-less Embedded System  
*Yu-Hsaing Yu, Jing-Zhong Wang, and Tsung-Ying Sun*
- 1100 Asynchronous Ring Network Mechanism with A Fair Arbitration Strategy for Network on Chip  
*Jih-Ching Chiu, Kai-Ming Yang, and Chen-Ang Wong*
- 1203 High-Performance 128-bit Comparator Based on Conditional Carry-Select Scheme  
*Shun-Wen Cheng, Jhen-Yuan Li, and Wei-Chi Chen*